

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	Group Art Unit: 2826
Yasuhiko TAKEMURA)	Examiner: Ahmed N. Sefer
Serial No. 09/342,235)	CERTIFICATE OF MAILING I hereby certify that this correspondence is
Filed: June 29, 1999)	being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450,
For: SEMICONDUCTOR DEVICE HAVING)	
AT LEAST FIRST AND SECOND)	Alexandria, VA 22313-1450, on June 26, 2007.
THIN FILM TRANSISTORS)	Rose Dichard

SUBMISSION OF FORMAL DRAWINGS

Honorable Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice of Allowability, submitted herewith are 15 sheets of Formal Drawings consisting of Figures 1-14.

Respectfully submitted,

Eric J. Robinson

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PMB 955

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